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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Torvalds et al. Examiner: Ellis, Richard L.

Serial No. 09/417,980 ) Art Unit: 2183

Filing Date: October 13, 1999

For: A METHOD FOR
TRANSLATING
INSTRUCTIONS IN A
SPECULATIVE
MICROPROCESSOR (As filed)

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Technology Center 2100

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

## AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Office Action mailed February 12, 2004, the following amendments and responses to the above captioned patent application are respectfully submitted. A request for continued examination has been filed herewith.

Serial No. 09/417,980 Examiner: Ellis, Richard L. Art Unit 2183 TRAN-P012